[0164] In addition, the third conductive film FOG**3** is disposed to correspond to the second area A**2'**. The third conductive film FOG**3** receives the second sub-side signal from the fourth fan-out lines PL**4** and applies the second sub-side signal to the second area A**2'**.

[0165] Consequently, the first area A**1'** receives the first sub-side signal output from the second pins P**2** through the second fan-out lines PL**2** and the second conductive film FOG**2**. Accordingly, the side display panel **200** displays a portion of the side image through the first area A**1'**.

[0166] Similarly, the second area A**2'** receives the second sub-side signal output from the fourth pins P**4** through the fourth fan-out lines PL**4** and the third conductive film FOG**3**. Therefore, the side display panel **200** displays a portion of the side image through the second area A**2'**.

[0167] As described above, the driving chip **310** applies the first and second sub-side signals through the second and fourth pins P**2** and P**4** as the second signal, and thus the second signal can be prevented from being distorted. For example, since the side display panel **200** is divided into the first area A**1** and the second area A**2**, a length in the first direction DR**1** or the third direction DR**3** of the gate lines provided in the first and second areas A**1'** and A**2'** is shortened. As a result, the signal can be prevented from being distorted due to the gate lines.

[0168] Although the inventive technology has been described, it is understood that the present invention should not be limited to these exemplary embodiments but various changes and modifications can be made by one ordinary skilled in the art within the spirit and scope of the present invention as hereinafter claimed.

What is claimed is:

1. A display device comprising:

a front display panel configured to display a front image in a front direction and having an edge at the perimeter that is curved when viewed from a front direction;

a side display panel that is curved and configured to display a side image in a side direction conforming to the edge of the front display panel when viewed from the front direction; and

a driver circuit board connected to the front and side display panels and configured to respectively apply first and second signals, corresponding to image data, to the front and side display panels.

2. The display device of claim **1**, wherein the front display panel comprises a front display surface crossing the front direction, and wherein the side display panel comprises a side display surface crossing the side direction.

3. The display device of claim **2**, wherein the side display surface is curved along the edge.

4. The display device of claim **1**, wherein the driver circuit board comprises a driving chip configured to generate the first and second signals.

5. The display device of claim **4**, further comprising:

a first conductive film configured to electrically connect the driving chip and the front display panel; and

a second conductive film configured to electrically connect the driving chip and a first end of the side display panel.

6. The display device of claim **5**, wherein the driving chip comprises:

a plurality of first pins electrically connected to the first conductive film; and

a plurality of second pins electrically connected to the second conductive film.

7. The display device of claim **6**, wherein the driving chip comprises a first side substantially parallel to a first direction and a second side substantially parallel to a second direction crossing the first direction, wherein the first pins are disposed on the first side, and wherein the second pins are disposed on the second side.

8. The display device of claim **7**, wherein the driver circuit board comprises a plurality of first fan-out lines each having first and second ends opposing each other and a plurality of second fan-out lines each having first and second ends opposing each other, wherein the first ends of the first fan-out lines are respectively connected to the first pins, wherein the second ends of the first fan-out lines are connected to the first conductive film, wherein the first ends of the second fan-out lines are respectively connected to the second pins, and wherein the second ends of the second fan-out lines are connected to the second conductive film.

9. The display device of claim **8**, wherein the driver circuit board comprises a plurality of third fan-out lines each having first and second ends opposing each other, wherein the driving chip comprises a plurality of third pins disposed on the first side, wherein the first ends of the third fan-out lines are respectively connected to the third pins, and wherein the second ends of the third fan-out lines are connected to the second conductive film.

10. The display device of claim **8**, wherein the first fan-out lines are arranged in the first direction, and wherein the second fan-out lines are arranged in the second direction.

11. The display device of claim **7**, wherein the side display panel surrounds the front display panel, wherein the first end of the side display panel is disposed adjacent to a first end of the driver circuit board in the first direction, and wherein a second end of the side display panel is disposed adjacent to a second end of the driver circuit board in a third direction opposite to the first direction.

12. The display device of claim **11**, further comprising a third conductive film configured to connect the driving chip and the second end of the side display panel.

13. The display device of claim **12**, wherein the driving chip comprises a plurality of fourth pins connected to the third conductive film.

14. The display device of claim **13**, wherein the driving chip comprises a third side substantially parallel to the second direction and disposed adjacent to the second side in the third direction, and wherein the fourth pins are disposed on the third side.

15. The display device of claim **14**, wherein the third conductive film is located farther from the front display panel than the second conductive film.

16. The display device of claim **15**, wherein the second signal comprises a first side signal to be applied to a first area of the side display panel and a second side signal to be applied to a second area of the side display panel different from the first area, wherein the second pins are configured to output the first side signal, and wherein the fourth pins are configured to output the second side signal.

17. The display device of claim **16**, wherein the first area is located adjacent to the second area in the second direction.

18. The display device of claim **16**, wherein the second area is located adjacent to the first area in the third direction.